

<b>Form PTO-1449</b> (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)	ATTY. DKT. NO. 5500-60900 APPLICANT: Hsu et al. FILING DATE: February 1, 2001	SERIAL NO. 09/776,080 GROUP: <del>2621</del> 2124
	<b>U.S. PATENT DOCUMENTS</b>	

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE APPROPRIAT

### FOREIGN PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATIO YES/NO

### OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

CD	A1	"Intel Architecture Software Developer's Manual, Vol. 2: Instruction Set Reference", Intel Corporation, 1999.
CD	A2	"Enhanced 3DNow!™ Technology for the AMD Athlon™ Processor," August 2000, Advanced Micro Devices, Inc., pp. 1-11.
CD	A3	"Inside 3DNow!™ Technology," © 2001 Advanced Micro Devices, Inc., pp. 1-4, <a href="http://www.amd.com/products/cpg/k623d/inside3d.html">http://www.amd.com/products/cpg/k623d/inside3d.html</a>
CD	A4	"3DNow!™ Technology in Microsoft DirectX 6.x," © 2001 Advanced Micro Devices, Inc. pp. 1-6, <a href="http://www.amd.com/products/cpg/3dnow/3ddirectx.html">http://www.amd.com/products/cpg/3dnow/3ddirectx.html</a>
CD	A5	"3DNow!™ Fact Sheet," © 2001 Advanced Micro Devices, Inc., p. 1, <a href="http://www.amd.com/products/cpg/3dnow/factsheet.html">http://www.amd.com/products/cpg/3dnow/factsheet.html</a>
CD	A6	"AMD 3DNow!™ Frequently Asked Questions," © 2001 Advanced Micro Devices, Inc., p. 1, <a href="http://www1.amd.com/products/cpg/result/1,1265,168,00.html">http://www1.amd.com/products/cpg/result/1,1265,168,00.html</a>
CD	A7	"3DNow!™ Technology Manual", © 2000 Advanced Micro Devices, Inc., pp. 1-62.
CD	A8	"AMD Extensions to the 3DNow!™ and MMX™ Instructions Sets Manual", © 2000 Advanced Micro Devices, Inc., pp. 1-36.

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Information Disclosure Statement--PTO 1449 (modified)

**Form PTO-1449** (modified)  
List of Patents and Publications  
For Applicant's Information  
Disclosure Statement  
(Use several sheets if necessary)

ATTY. DKT. NO. 5500-60900

SERIAL NO. 09/776,080

APPLICANT: Hsu et al.

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## U.S. PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE APPROPRIAT
	B1	6,421,696	7/16/02	Horton	<b>RECEIVED</b> NOV 06 2003 Technology Center 2100		
CD	B2	6,397,235	5/28/02	Van Eijndhoven, et al			
CD	B3	6,317,767	11/13/01	Wang			
	B4	6,141,673	10/31/00	Thayer, et al			
	B5	6,067,099	5/23/00	Kabir, et al			
CD	B6	6,038,580	3/14/00	Yeh			
	B7	6,018,351	1/25/00	Mennemeier, et al			
CD	B8	5,984,515	11/16/99	Mennemeier, et al			
	B9	5,815,421	9/29/98	Dulong, et al			
CD	B10	5,610,849	3/11/97	Huang			
CD	B11	5,596,517	1/21/97	Jones, et al			
CD	B12	5,590,066	12/31/96	Ohki			
CD	B13	5,754,456	5/19/98	Eitan, et al			

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## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	B14	<del>"A Cost Effective Architecture for 8 X 8 Two-Dimensional DCT/IDCT Using Direct Method", Lee, et al, IEEE Transactions on Circuits and Systems for Video Technology, Vol. 7, No. 3, June 1997</del> duplicate U					
	B15	<del>"Statistical Inverse Discrete Cosine Transforms for Image Compression", Hung, et al, Proceedings of the SPIE Symposium of Electronic Imaging, Vol. 2187, pp. 196-205, 1994.</del>					
	B16	<del>"Practical Fast 1-D DCT Algorithms with 11 Multiplications", Loeffler, et al, Proc. Int'l. Conf. on Acoustics, Speech, and Signal Processing, 1989 (ICASSP '89), pp. 988-991</del>					
	B17	<del>"The Design and Implementation of DCT/IDCT Chip with Novel Architecture", Cheng, et al, IEEE International Symposium on Circuits and Systems, May 28-31, 2000, Geneva, Switzerland.</del> duplicate V					

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